



SIMATIC DP, Electronics module f. ET200S, 2AI TC High Feature, 15 mm width, 15 bit+sign with internal temperature Compensation

Supply voltage	
Load voltage L+	
• Rated value (DC)	24 V; From power module
• Reverse polarity protection	Yes
Input current	
from load voltage L+ (without load), max.	30 mA
from backplane bus 3.3 V DC, max.	10 mA
Power loss	
Power loss, typ.	0.6 W
Address area	
Address space per module	
• Address space per module, max.	4 byte
Analog inputs	
Number of analog inputs	2
permissible input voltage for voltage input (destruction limit), max.	20 V; ± 20 V, continuous
Cycle time (all channels) max.	Number of active channels per module x basic conversion time
Technical unit for temperature measurement adjustable	Yes; Celsius / Fahrenheit
Input ranges (rated values), voltages	
• -80 mV to +80 mV	Yes
— Input resistance (-80 mV to +80 mV)	1 M Ω
Input ranges (rated values), thermocouples	
• Type B	Yes
— Input resistance (Type B)	1 M Ω
• Type C	Yes
— Input resistance (Type C)	1 M Ω
• Type E	Yes
— Input resistance (Type E)	1 M Ω
• Type J	Yes
— Input resistance (type J)	1 M Ω
• Type K	Yes
— Input resistance (Type K)	1 M Ω
• Type L	Yes
— Input resistance (Type L)	1 M Ω
• Type N	Yes
— Input resistance (Type N)	1 M Ω
• Type R	Yes
— Input resistance (Type R)	1 M Ω
• Type S	Yes
— Input resistance (Type S)	1 M Ω

<ul style="list-style-type: none"> • Type T 	Yes
<ul style="list-style-type: none"> — Input resistance (Type T) 	1 MΩ
Thermocouple (TC)	
Temperature compensation	
<ul style="list-style-type: none"> — internal temperature compensation 	Yes; possible with TM-E15S24-AT, TM-E15C24-AT
<ul style="list-style-type: none"> — external temperature compensation with compensations socket 	Yes; one external compensating box per channel
Characteristic linearization	
<ul style="list-style-type: none"> • parameterizable 	Yes
<ul style="list-style-type: none"> — for thermocouples 	Type B, C, E, J, K, L, N, R, S, T to IEC 584
Cable length	
<ul style="list-style-type: none"> • shielded, max. 	50 m
Analog value generation for the inputs	
Measurement principle	integrating
Integration and conversion time/resolution per channel	
<ul style="list-style-type: none"> • Resolution with overrange (bit including sign), max. 	16 bit
<ul style="list-style-type: none"> • Integration time (ms) 	16,7 / 20 ms
<ul style="list-style-type: none"> • Interference voltage suppression for interference frequency f1 in Hz 	50 / 60 Hz
<ul style="list-style-type: none"> • Conversion time (per channel) 	66 ms; 66 / 80 ms; additional conversion time for diagnostic wire break test
Smoothing of measured values	
<ul style="list-style-type: none"> • parameterizable 	Yes; In four stages by means of digital filtering
<ul style="list-style-type: none"> • Step: None 	Yes; 1x cycle time
<ul style="list-style-type: none"> • Step: low 	Yes; 4x cycle time
<ul style="list-style-type: none"> • Step: Medium 	Yes; 32x cycle time
<ul style="list-style-type: none"> • Step: High 	Yes; 64x cycle time
Errors/accuracies	
Operational error limit in overall temperature range	
<ul style="list-style-type: none"> • Voltage, relative to input range, (+/-) 	0.1 %; ±1.5 K for thermocouples, ±7 K for thermocouples type C, ±2.5 K with static thermal state (ambient temperature change < 0.3 K/min)
Basic error limit (operational limit at 25 °C)	
<ul style="list-style-type: none"> • Voltage, relative to input range, (+/-) 	0.05 %; ±1 K with thermocouples, ±5 K with thermocouples type C, ±1.5 K with static thermal state (ambient temperature change < 0.3 K/min)
Interrupts/diagnostics/status information	
Diagnoses	
<ul style="list-style-type: none"> • Wire-break 	Yes; only thermocouples
<ul style="list-style-type: none"> • Group error 	Yes
<ul style="list-style-type: none"> • Overflow/underflow 	Yes
Diagnostics indication LED	
<ul style="list-style-type: none"> • Group error SF (red) 	Yes
Parameter	
Remark	4 byte
Diagnostics wire break	Disable / enable (wire break is detected only in thermocouples)
Group diagnostics	Disable / enable
Overflow/underflow	Disable / enable
Comparison point	none / yes, internal
Potential separation	
Potential separation analog inputs	
<ul style="list-style-type: none"> • between the channels 	No
<ul style="list-style-type: none"> • between the channels and backplane bus 	Yes
<ul style="list-style-type: none"> • Between the channels and load voltage L+ 	Yes
Isolation	
Isolation tested with	500 V DC
Dimensions	
Width	15 mm
Height	81 mm
Depth	52 mm
Weights	
Weight, approx.	40 g

last modified:

8/16/2023 